



## Teradyne L393 Functional Board Test System

Channel card slot configuration as follows:

```
slot [ 0 ] id [ 102040 ] :User utility interface board
slot [ 3 - 7 ] id [ 177234 ] :D7 24 channel (32K)
slot [ 8 - 9 ] id [ 177234 ] :D6 12 channel (32k)
slot [ 10 - 10 ] id [ 177234 ] :D7 24 channel (32K)
```

```
[ Left Side ] id [ none ] :Digital backplane buffer board
```

Analog instrumentation / matrix configuration as follows:

```
slot [ 2 ] id [ 132 ] :signals
slot [ 3 ] id [ 374 ] timer / counter digital
slot [ 4 ] id [ FF01 ] :timer High resolution
slot [ 5 ] id [ FF09 ] time measurement analog
slot [ 6 ] id [ FF09 ] :dcms analog
slot [ 8 ] id [ FF03 ] :dcms digital
slot [ 10 ] id [ FF00 ] :dc v source
Slot [ 12 ] id [ 30 ] waveform gen
slot [ 13 ] id [ 177 ] wave form digital
slot [ 14 ] id [ 100 ] dc source
slot [ 18 ] id [ 2 ] meter digital
slot 20 ] id [ 130 ] analog house keeper
```

Utility card slot configuration as follows:

```
slot [ 2 ] id [ 1 ] :Channel card references
slot [ 4 ] id [ 1 ] :Channel card references
slot [ 16 ] :Utility board
slot [ 21 ] :L290 pattern controller
slot [ 22 ] :L290 Data Acquesition cont,
slot [ 23 ] id [ 7421 ] :L350 Timing controller phase/window 1 - 4
slot [ 24 ] id [ 7411 ] :L350 Timing controller period generator (64K)
slot [ 25 ] :L350 Timing control phase / window 5 - 8
slot [ 26 ] : Digital Matrix
slot [ 27 ] : Probe timing generator
slot [ 32 ] :General Interface
```

Ref # BDT42  
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