



## Teradyne Catalyst Test System

```
# Confsim file created on: 04/07/08 11:15:47

# Catalyst tester

#           is a 2 processor system
# Processor 1: 450 MHz spare (online)
# Processor 3: 450 MHz sparac (online)
# PCI based system
# Terabus is present
#           Board ID
#           Serial number
# TCI is present

CATALYST_TH 1
BACKPLANE A
#Slot Type      Num      XptA XptB Name
  2  879-858-23  0      # 23 24  REFSRC CC
  3  949-658-00  0      # 21 22  LFAC DUAL CC
  4  949-894-00  0      # 19 20  QVS CC
  5  879-792-01  0      # 17 18  TIME CC
  6  949-831-50  0      # 15 16  1G VHFD CC
  7  949-818-50  0      # 13 14  VHFAWG2500 CC
  8  879-857-01  0      # 11 12  PMM CC-01
  9  000-000-00  0      # 9 10  EMPTY
 10  000-000-00  0      # 7 8  EMPTY
 11  000-000-00  0      # 5 6  EMPTY
 14  000-000-00  0      # 25 26  EMPTY
 15  000-000-00  0      # 27 28  EMPTY
 16  803-595-00  0      # 29 30  UWMM
 17  879-906-51  0      # 31 32  VHFCW CC
 18  803-596-05  0      # 33 34  UWPORT
 19  803-594-00  0      # 35 36  UHFSRC CC
 20  879-909-00  0      # 37 38  SCC MSTR
 21  000-000-00  0      # 39 40  EMPTY
 22  949-658-00  0      # 41 42  LFAC DUAL CC
 23  949-643-00  0      # 43 44  TJD CC
  1  949-669-00  0      # 3 0  HAS (Left HAS LA669-00)
 13  949-668-00  0      # 0 0  CATALYST HAC
 12  949-667-00  0      # 0 0  DIF
 24  949-669-01  0      # 4 0  HAS (Right HAS LA669-01)
END

RF_PIPES 1
# pipe-name slot schan description
  Z4A  18  1  # UWPORT (OSP)
  Z3A  18  2  # UWPORT (OSP)
  Z2A  18  3  # UWPORT (OSP)
  Z1A  18  4  # UWPORT (OSP)
  Y15B  7  1  # VHFAWG2500 CC (OSSP)
  Y17B  7  2  # VHFAWG2500 CC (OSSP)
  Y16B  7  3  # VHFAWG2500 CC (OSSP)
  Y18B  7  4  # VHFAWG2500 CC (OSSP)
  Z12C  6  9  # 1G VHFD CC (OSP)
  Z12D  6  8  # 1G VHFD CC (OSP)
END
```

Ref # Catalyst - 0824j  
Contact: Tom Gerst - [tomg@jmcserv.com](mailto:tomg@jmcserv.com)  
+(505) 798-1649



## Teradyne Catalyst Test System (continued)

```

RF_CONNECTIONS 1
# RF pipe connections between channel cards
# slot schan slot schan
# 19 1 to 18 5
# 16 1 to 18 7
END

# Up to 4 Precision AC Card Cages are allowed
#
PRECISION_AC 1
*Slot Type Num Name
1 949-660-01 0 # LFACDIG
2 949-659-00 0 # LFACSRC
3 949-664-00 0 # VHFDIG MF
4 949-819-00 0 # VHFAPWG2500
5 000-000-00 0 # EMPTY
6 000-000-00 0 # EMPTY
7 000-000-00 0 # EMPTY
8 949-671-01 0 # PACS CAGE INT
END

PRECISION_AC 2
#Slot Type Num Name
1 949-660-01 0 # LFACDIG
2 949-659-00 0 # LFACSRC
3 000-000-00 0 # EMPTY
4 000-000-00 0 # EMPTY
5 949-320-00 0 # UWMS
6 949-827-00 0 # VHFAPWG
7 000-000-00 0 # EMPTY
8 949-671-01 0 # PACS CAGE INT
END

#
# Up to 8 Universal Backplane/Synch Power Subsystem
# cages are allowed
#
# For the Synch Power Subsystem:
# Slot Type Name Instr1 # Instr2 # Ammeter #
#
Instr1 # - instrument connected to the last two matrix line
Instr2 # - instrument connected to the last two matrix line
Ammeter # - ammeter connection
to AVOID errors, put NO 0 if no instrument is connected.

UB_SPS_CAGE 1
# Slot Type Num Name
1 879-802-02 0 # UB_SPS_802
2 517-301-01 0 # UB_MATRIX
3 517-301-01 0 # UB_MATRIX
4 517-301-01 0 # UB_MATRIX
5 517-301-01 0 # UB_MATRIX
6 517-301-01 0 # UB_MATRIX
7 517-301-01 0 # UB_MATRIX
8 517-301-01 0 # UB_MATRIX
9 517-301-01 0 # UB_MATRIX
10 517-301-01 0 # UB_MATRIX
11 517-301-00 0 # UB_APU

```

Ref # Catalyst – 0824j  
 Contact: Tom Gerst - [tomg@jmcserv.com](mailto:tomg@jmcserv.com)  
 +(505) 798-1649



## Teradyne Catalyst Test System (continued)

```

12 517-301-00 0 # UB_APU
13 517-301-00 0 # UB_APU
14 879-925-01 0 # UB_60_V_SRC MAT 1
15 879-925-01 0 # UB_60_V_SRC DUT 1
16 879-925-01 0 # UB_60_V_SRC DUT 2
17 879-925-01 0 # UB_60_V_SRC DUT 7
18 879-925-01 0 # UB_60_V_SRC DUT 8
19 879-925-01 0 # UB_60_V_SRC DUT 5
20 879-925-01 0 # UB_60_V_SRC DUT 6
21 879-690-00 0 # UB_ASY
22 517-300-01 0 # UB_TJ300
END

```

```

UB_OPS_CAGE 2
# Slot Type Num Name
1 879-802-02 0 # UB_SPS_802
2 949-700-10 0 # UB_QVS_CAL 1
3 949-693-10 0 # UB_QVS_CTRL 4 ??
4 949-698-10 0 # UB_QVS_AM 1
5 949-698-10 0 # UB_QVS_AM 2
12 879-925-01 0 # UB_60_V_SRC MAT 2
22 517-300-01 0 # UB_TJ300
END

```

```

HSD100_CHAN_CAGE 1
#Slot Type Num Fld1 Fld2 Name
1 949-921-01 0 # HSD CDM 400
2 949-921-01 0 # HSD CDM 400
3 949-921-01 0 # HSD CDM 400
4 949-921-01 0 # HSD CDM 400
5 949-820-10 0 # HSD CSB
6 949-921-01 0 # HSD CDM 400
7 949-921-01 0 # HSD CDM 400
8 949-921-01 0 # HSD CDM 400
9 949-921-01 0 # HSD CDM 400
END

```

```

HOD100_CHAN_CAGE 2
#Slot Type Num Fld1 Fld2 Name
1 949-921-01 0 # HSD CDM 400
2 949-921-01 0 # HSD CDM 400
3 949-921-01 0 # HSD CDM 400
4 949-921-01 0 # HSD CDM 400
5 949-820-00 0 # HSD CSB
6 949-921-01 0 # HSD CDM 400
7 949-921-01 0 # HSD CDM 400
8 949-921-01 0 # HSD CDM 400
9 949-921-01 0 # HSD CDM 400
END

```

```

HOD100_CHAN_CAGE 3
#Slot Type Serial # Num Fld1 Fld2 Name
5 949-820-00 0 # HSD CSB
END

```

Ref #      Catalyst – 0824j Contact:   Tom Gerst - <a href="mailto:tomg@jmcserv.com">tomg@jmcserv.com</a> +(505) 798-1649
---



## Teradyne Catalyst Test System (continued)

```
CATALYST_TH 1
BACKPLANE B
#Slot Type      Num      Name
25  949-625-00  0  # HSD DTH
26  949-625-00  0  # HSD DTH
27  949-625-00  0  # HSD DTH
28  949-625-00  0  # HSD DTH
29  949-626-10  0  # HSD THS
30  949-625-00  0  # HSD DTH
31  949-625-00  0  # HSD DTH
32  949-625-00  0  # HSD DTH
33  949-625-00  0  # HSD DTH
38  949-626-10  0  # HSD THS
43  949-625-00  0  # HSD DTH
44  949-625-00  0  # HSD DTH
45  949-625-00  0  # HSD DTH
46  949-625-00  0  # HSD DTH
47  949-626-20  0  # HSD THS/HCLK
48  949-625-00  0  # HSD DTH
49  949-625-00  0  # HSD DTH
50  949-625-00  0  # HSD DTH
51  949-625-00  0  # HSD DTH
56  949-626-10  0  # HSD THS
61  949-625-00  0  # HSD DTH
62  949-625-00  0  # HSD DTH
63  949-625-00  0  # HSD DTH
64  949-625-00  0  # HSD DTH
65  949-626-00  0  # HSD THS
66  949-625-00  0  # HSD DTH
67  949-625-00  0  # HSD DTH
68  949-625-00  0  # HSD DTH
69  949-625-00  0  # HSD DTH
70  949-625-00  0  # HSD DTH
71  949-625-00  0  # HSD DTH
72  949-625-00  0  # HSD DTH
73  949-625-00  0  # HSD DTH
74  949-626-10  0  # HSD THS
75  949-625-00  0  # HSD DTH
76  949-625-00  0  # HSD DTH
77  949-625-00  0  # HSD DTH
78  949-625-00  0  # HSD DTH
END

# Trigger Switch Yard

# Note: The logical slot numbers below correspond to the physical slot
# numbers only for test systems which contain a TSY card
# cage. In test systems which contain an SCS/TSY card cage the
# mapping is:

Logical TSY slot (below) Physical slot
1                ->      2
3                ->      1
```

Ref # Catalyst – 0824j  
Contact: Tom Gerst - [tomg@jmcserv.com](mailto:tomg@jmcserv.com)  
+(505) 798-1649



## Teradyne Catalyst Test System (continued)

```

TSY CAGE
#Slot Type      Num      Name
  1  879-655-02  0      # TSY
  2  000-000-00  0      # EMPTY
  3  879-671-01  0      # T_DELAY
  4  000-000-00  0      # EMPTY
END

# Time Subsystem

TIME_SUBSYSTEM
# Board ID      Name
  879-793-00    # TMS Timer
  879-794-01    # TMS Counter
  879-795-01    # TMS Support
  949-782-00    # Time Mux Board 1
END

# The UWMS option resides in the PACS cage and is composed of two
# plug-in modules that may reside in one of three locations A, B or C.
# A zero in the type field indicates no plug-in module.
# PACS_UW <n> - indicates PACS cage number of these UWMS options
# Slot - indicates slot number and module position for this UWMS option
# Type - board identification number
# AWG Num - AWG instrument number connected to this UWMS option
# UHFSRC Num - UHF instrument number that has this UWMS option
# MODSRC Num - First or second MODSRC connected to above UHFSRC

PACS UW 2
                                AWG UHFSRC MODSRC
*Slot Type      Rev      Date      Num Num      Num      Name
  5  949-320-00  A      0122-0    2      1      1      # UWMS
  5A 949-330-00  A      9944-0                    # UWMS UP CONV
  5B 000-000-00  0      0000-0                    #
  5C 949-333-00  C      0017-0                    # UWMS MUX
END

# DC Subsystem -
# SRC <NUM>          [1 - 13]
      (sources 1-5 are MATRIX sources 1-5
      sources 6-13 are DUT sources 1-8)
HCU <NUM>          *[1 - 4]
REF HCU <NUM>      *[1 - 4]
HVSRC <NUM>        *[1 - 4]
PWSRC <NUM>        [1 - 4]
DATABITS <NUM> - <NUM> [1 - 192]

# ** These instruments share the same seven-slot cage -- only one
# instrument is allowed per slot.

```

Ref #      Catalyst – 0824j  
 Contact:    Tom Gerst - [tomg@jmcserv.com](mailto:tomg@jmcserv.com)  
             +(505) 798-1649



## Teradyne Catalyst Test System (continued)

```
DC_SUBSYSTEM
# UBVI 60 1 ( 60V V/I Source in Universal Backplane 1 : slot 14)
# UBVI 60 2 ( 60V V/I Source in Universal Backplane 2 : slot 12)
# UBVI 60 6 ( 60V V/I Source in Universal Backplane 1 : slot 15)
# UBVI 60 7 ( 60V V/I Source in Universal Backplane 1 : slot 16)
  HCU 8
  HCU 9
# UBVI 60 10 ( 60V V/I Source in Universal Backplane 1 : slot 19)
# UBVI 60 11 ( 60V V/I Source in Universal Backplane 1 : slot 20)
# UBVI 60 12 ( 60V V/I Source in Universal Backplane 1 : slot 17)
# UBVI 60 13 ( 60V V/I Source in Universal Backplane 1 : slot 18)
  DATABITS 1 - 48

# UB MATRIX
#
# Testhead 1
# XPTs UB Cage Slot Type
# 1-4 1 2 Matrix
# 5-8 1 3 Matrix
# 9-12 1 4 Matrix
# 13-16 1 5 Matrix
# 17-20 1 6 Matrix
# 21-24 1 7 Matrix
# 25-28 1 8 Matrix
# 29-32 1 9 Matrix
# 33-36 1 10 Matrix
# 37-40 1 11 APU
# 41-44 1 12 APU
# 45-48 1 13 APU
END
```

Ref # Catalyst – 0824j  
Contact: Tom Gerst - [tomg@jmcserv.com](mailto:tomg@jmcserv.com)  
+(505) 798-1649