



Teradyne Catalyst D200 Mixed Signal Test System

Software

OS	Solaris 8
Image	8.3D2, 8.3, 7.0D11

Hardware

User Computer	Sunblade 1500
Tester Computer	Sunblade 2500

DC Subsystem

UBVI src	7
Std Src	2
HCU	4
AAPU	48
Matrix Pin	48
Stored Data bits	48

Digital

No. of Channels	320
Backplane ready	384
Pico Clock	
Gazelle	
SCAN	24 chain X 32M
LVM	16
Mhz (DRATE)	200
MTO	--
PROGEN	--
DATAPUMP	--
PNA	--
BBAC SRC License	--
BBAC DIG License	--

Analog

PACS II Backplane	2
PLFD	2,23
PLFS	3,22
REFSRC	--
VREG	--
LFAC	--
BBAC	--
Time CC	5

Ref #	Catalyst – 0822-1
Contact:	Tom Gerst - tomg@jmcserv.com +(505) 798-1649



Teradyne Catalyst D200 Mixed Signal Test System (continued)

PMM	--
VHFDIG 1GHz D/C	11
VHFDIG 1GHz	--
VHFDIG 600MHz D/C	--
VHFDIG 600MHz	--
VHFAWG 400 DIFF	10
VHFAWG 1200	7
VHFAWG 2500	--
VHFCW	16
HSS	20
TJD	18
TMB	yes
TMS	yes
UHFSRC (3.2GHz)	--
UHFSRC (6GHz)	--
UHFSRC MS (6GHz)	--
UHFMM	--
UHFPORT (Quad)	--
UHFPORT (Octal)	--
Noise Src	--
Freq Hopping	--
Quad Sync	2
TSY	yes
T_Delay	yes
Options	
Expansion Cabinet	1
Manipulator	1

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