



Credence STS-5020 Test System

- 7.1.1 TEST SYSTEM COMPUTER OF STS5020 SYSTEM
- 7.1.2 Computer type Sun 50Mhz SPARCstation 20
- 7.1.3 Memory 64 MB dynamic RAM memory
- 7.1.4 Bus architecture
 - one MBUS
 - 32-bit VME backplane bus
 - SCSI controller bus
 - IEEE-488 bus
- 7.1.5 Main Mass Storage
 - 1.05 GByte (minimum) internal disk drive
 - 150 MB streaming tape drive
 - Standard double-speed CD-ROM drive
- 7.1.6 System Software
 - Solaris 1.1.1 B Operating System
 - Sun Pascal Programming language
 - Credence 5020 System Software release: DM7.1.3 DM7.1.3.
- 7.1.7 Communication
 - Ethernet via TCP/IP (ISO 8802-3), NFS, X, SCSI, IEEE-488, RS-232
- 7.1.8 Peripherals
 - Turbo CX 1MB frame buffer
 - Operator keypad
 - 4 RS232 serial I/O ports
 - 1 RS232 modem port
 - Centronics parallel port
 - 4 IEEE-488 bus controllers
 - Ethernet interface
 - Handler or prober optical interface
 - VT100 compatible production terminal/RS-232 port
- 7.2 ANALOG SUBSYSTEM CONFIGURATION
 - AC Source and Measure Module (60-1017)
 - Precision V/I Source 5 V/I Sources (90-0107)
 - Digital Relay I/O Module 48 Drive/Detect Channels (90-0112)
 - Kelvin Matrix Module 32 (4 x 8) lines (90-0113)
 - Digitizing Voltmeter (60-1016-20)
 - Time Measurement Unit (90-0158-30)
 - NIST-Traceable Calibrator, HP3457 (90-0170)
 - Telecom Filter Module (90-0182)
 - Dual Channel Waveform Generator (90-203-02)
 - 10MHz Waveform Digitizer (90-0206-02)
- 7.3 DIGITAL SUBSYSTEM CONFIGURATION
 - Digital Pins (Digital Waveform Pattern Processor)
 - 16-32 digital pins (90-0201)
 - Parametric Measurement Unit 32 Channels (2 x 16) (90-173)
- 7.4 PROGRAMMABLE TEST HEAD (PTH) CONFIGURATION
 - General Purpose Relay Driver module (80-3115)
 - 2 boards, 16 channel per board
 - Video Front End Module (80-3134)
 - Digitizing Voltmeter Front End Module (80-3118)
 - Waveform Generator Back End Module (80-3123)

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