



Advantest T5581H Memory Test System

SYSTEM CONFIGURATION GENERATE

CONFIGURATION OF TEST HEAD

PIN CONFIGURATION 1.640DR+288I/O
 2.960DR+288I/O
 3.640DR+576I/O
 4.960DR+576I/O [1-4]> 4

TH CABLE LENGTH 1.5M
 2.8M [1,2]> 1

NUMBER OF TEST HEAD [1,2]> 1

CONFIGURATION OF TEST HEAD 1

TEST HEAD TYPE 1.816CH
 2.1600CH [1,2]> 2

PE BOARD TYPE 0.NO BOARD
 1.BGM-021633 (FULL PE)
 2.BGM-021633X02 (I/O PE)
 3.BGM-021633X03 (DR PE)

	SLOT	33	36	39	161	164	167
CHILD A>	1	1	1	1	1	1	1
CHILD B>	1	1	1	1	1	1	1
CHILD C>	1	1	1	1	1	1	1
CHILD D>	1	1	1	1	1	1	1
CHILD E>	1	1	1	1	1	1	1
CHILD F>	1	1	1	1	1	1	1
CHILD G>	1	1	1	1	1	1	1
CHILD H>	1	1	1	1	1	1	1

CONFIGURATION OF DPU

1'ST DPU : STN 1,2 [Y,N]> YES
 2'ND DPU : STN 1,2 [Y,N]> YES

TEST HEAD 1 DC CONFIGURATION [1-32]> 1-32
 10V PPS SPEC [1,2] (1:0.4A 2:0.8A)> 2
 10V PPS CONFIGURATION [1-64]> 1-64
 16V PPS CONFIGURATION [33-64]>
 HV PPS CONFIGURATION [1-4]> 1-4

1'ST GPIB I/F BOARD 0.NONE
 1.BGR-010944X01
 2.BGR-010944X02
 3.BGR-010944X03
 4.BGR-010944X04
 5.BGK-012718 [0-5]> 0

2'ND GPIB I/F BOARD 0.NONE
 1.BGR-016793 (DPU I/F)
 2.BGR-010944X05
 3.BGK-012718X02 [0-3]> 1

Ref # T5581H (a) - 0918
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Advantest T5581H Memory Test System (continued)

DMM TYPE 1.TR6861
 2.R6871E
 3.R6551
 4.R6552T [1-4]> 4

AC FREQUENCY (Hz) [50,60]> 50

CONFIGURATION OF ALPG
 NUMBER OF ALPG [1,2,4]> 2

CONFIGURATION OF DBM
 DBM OPTION EXIST [Y,N]> NO

CONFIGURATION OF FM
 NUMBER OF FM BOARD [0-4]> 4
 SIZE OF AFM BOARD [0-2] [0:256K 1:1M 2:4M]> 2
 PATTERN MEMORY (PM) BOARD EXIST [Y,N]> NO

CONFIGURATION OF MRA
 MRA2/3 OPTION [Y,N]> NO

END SAVE

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